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PATTERSON & SHERIDAN, LLP
Gero McClellan / Infineon Technologies
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Joseph Jong

(Depositor's name)

(Signature)

May 1, 2006

(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/808,219	03/24/2004	Peter Hagemeyer	INFN/0060	3942

TITLE OF INVENTION: FLASH MEMORY CELL WITH BURIED FLOATING GATE AND METHOD FOR OPERATING SUCH A FLASH MEMORY CELL

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	05/01/2006
EXAMINER	ART UNIT		CLASS-SUBCLASS		
PHAM, LONG	2814		257-315000		

Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).
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(B) RESIDENCE: (CITY and STATE OR COUNTRY)

INFINEON TECHNOLOGIES AG

MUNICH, GERMANY

Please check the appropriate assignee category or categories (will not be printed on the patent): Individual Corporation or other private group entity Government

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Change in Entity Status (from status indicated above)

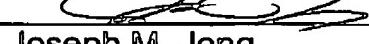
a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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Date May 1, 2006

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Joseph M. Jong

Registration No. 42,698

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→ USPTO ISSUE FEE #001/003

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FACSIMILE COVER SHEET

DATE: May 1, 2006
FILE NO: INFN/WB0060 (INF 2227-PC/US)
TO: Mail Stop ISSUE FEE
Commissioner for Patents, USPTO
FAX NO: 1-571-273-2885
FROM: Gero G. McClellan / Joseph M. Jong
PAGE(S) with cover: 3
ORIGINAL TO FOLLOW? YES NO

RE:
TITLE: FLASH MEMORY CELL WITH BURIED FLOATING GATE AND
METHOD FOR OPERATING SUCH A FLASH MEMORY CELL
U.S. SERIAL NO.: 10/808,219
FILING DATE: March 24, 2004
INVENTOR: Hagemeyer et al.
EXAMINER: Long Pham
ART UNIT: 2814
CONF. NO.: 3942

Attached are:

1. Part B - Fee(s) Transmittal for Application No. 10/808,219
2. Copy for charge to Deposit Account No. 20-0782 / INFN/WB0060 / GGM

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